

T.J. GABARA 91-2-36 SERIAL NO. 10/719,193

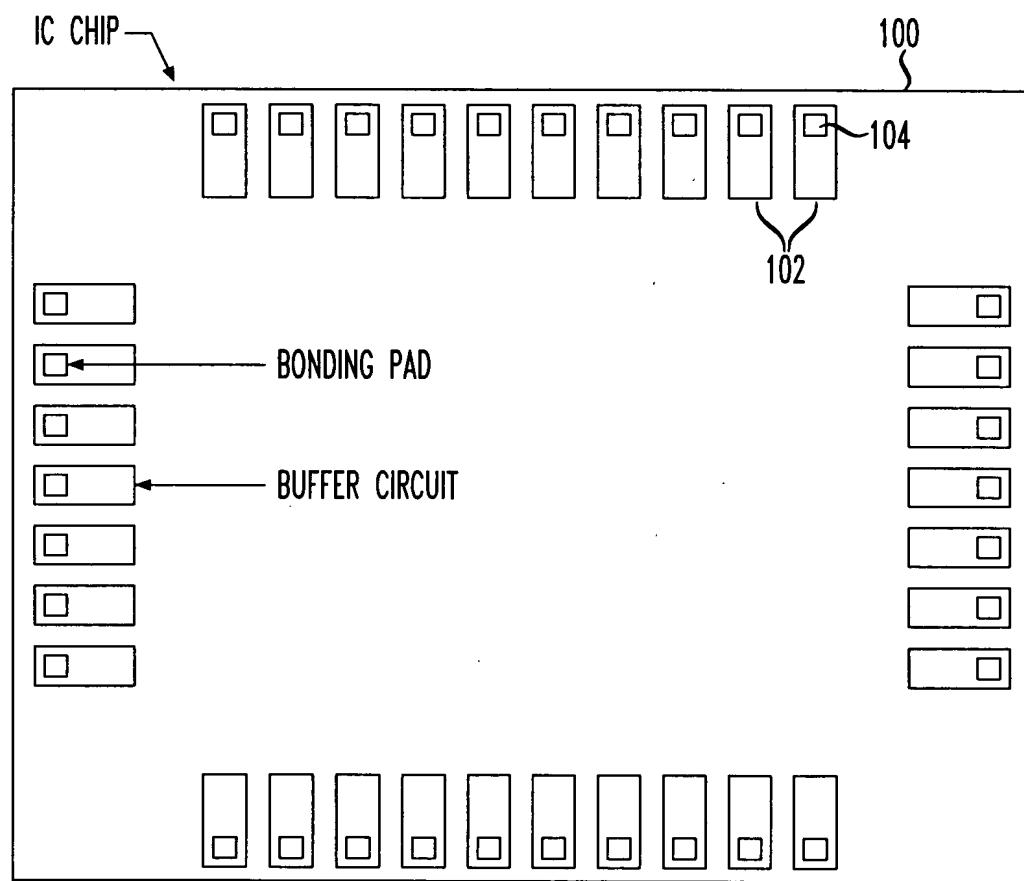
INTEGRATED CIRCUIT WITH CONTROLLABLE TEST ACCESS TO INTERNAL ANALOG SIGNAL PADS OF AN AREA ARRAY

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FIG. 1
(PRIOR ART)



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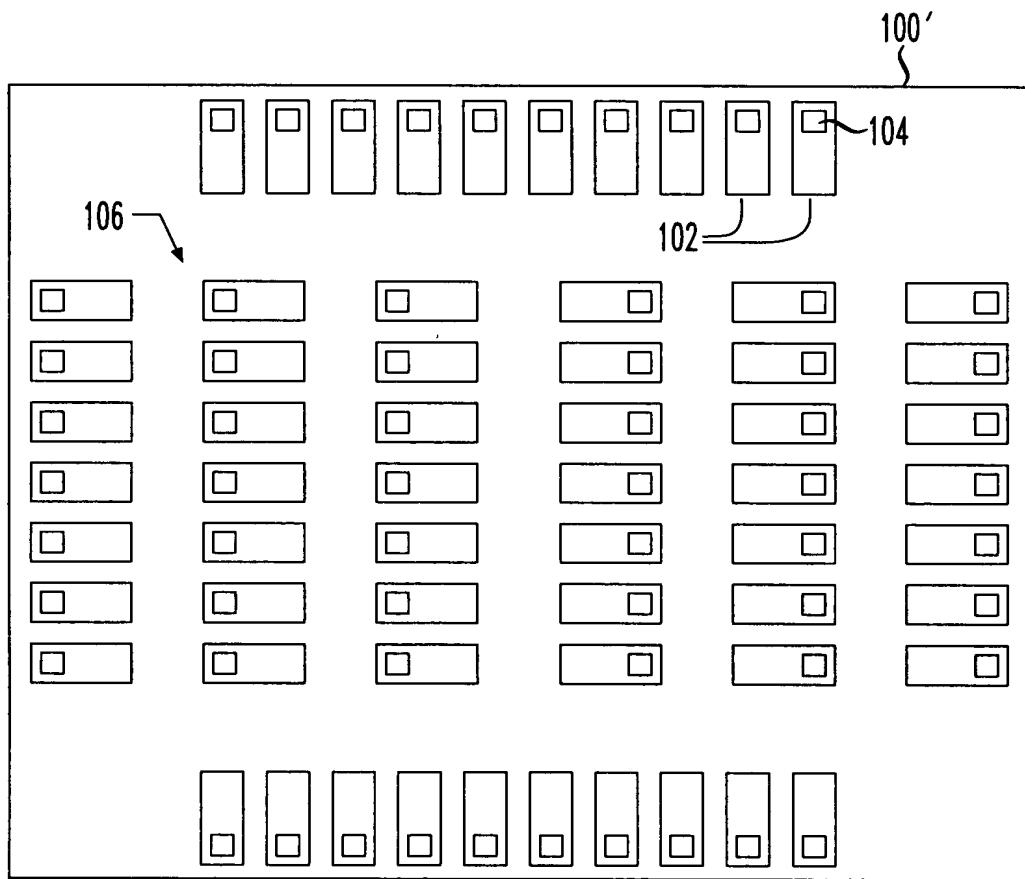
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FIG. 2
(PRIOR ART)



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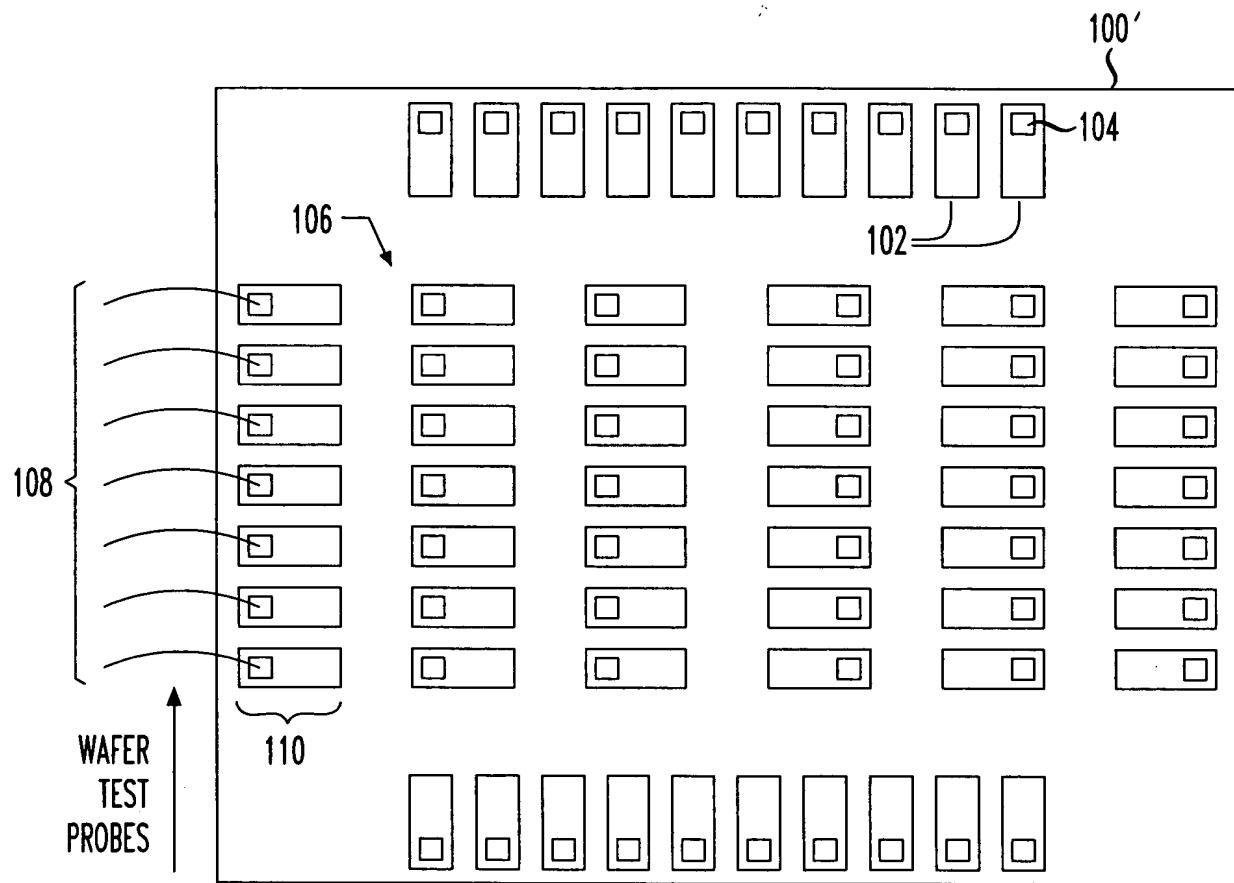
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FIG. 3

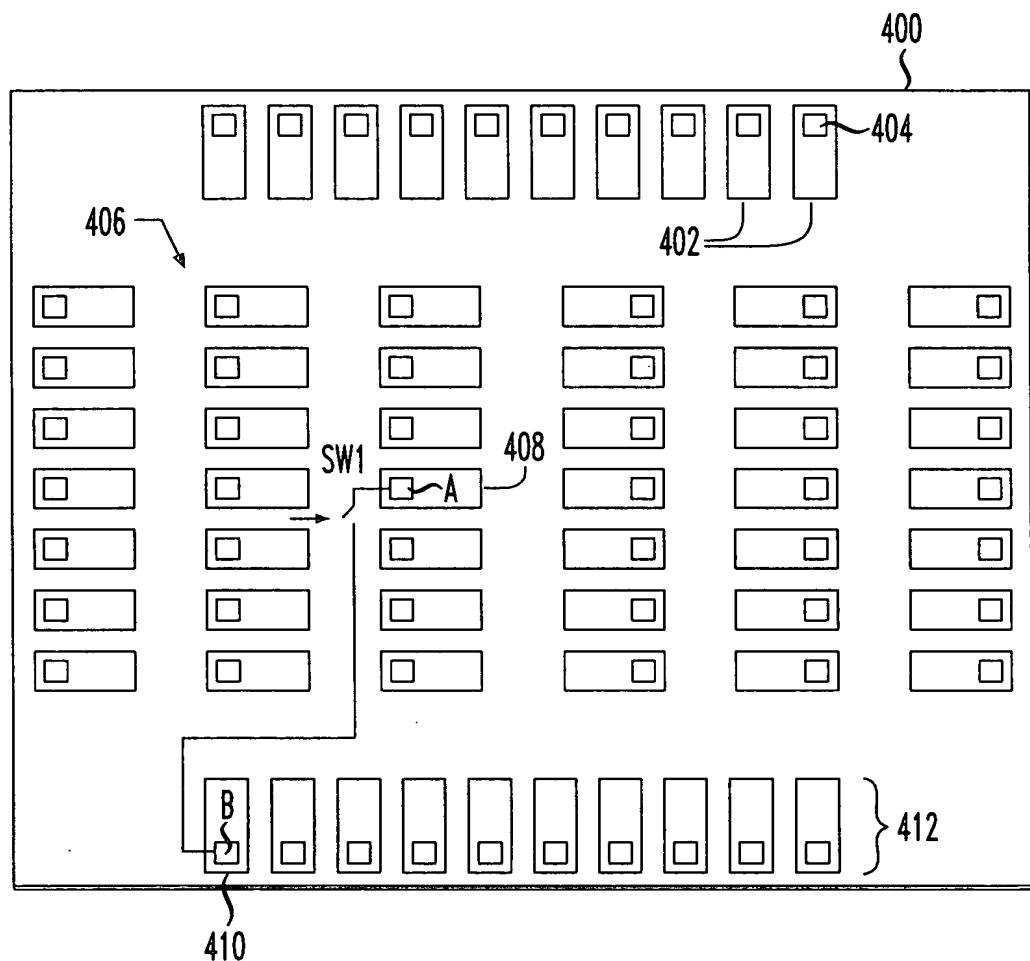
(PRIOR ART)





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FIG. 4



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FIG. 5

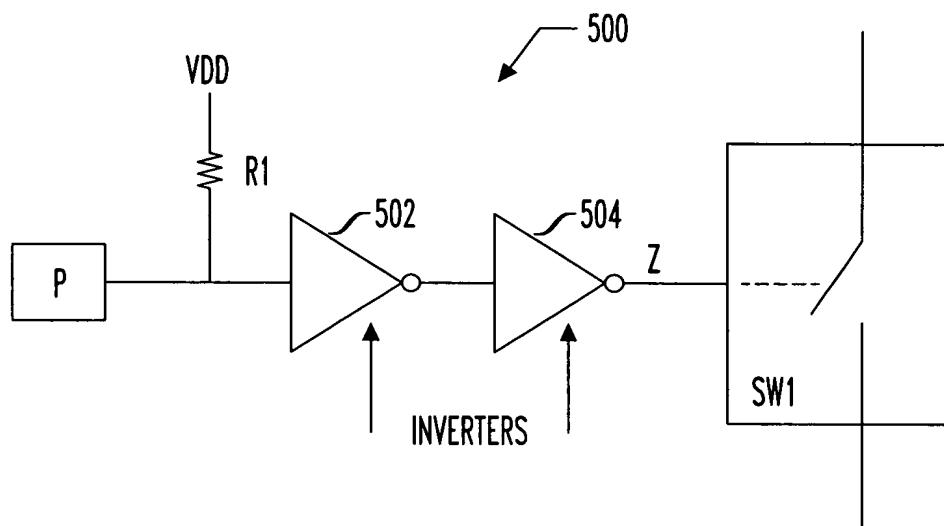
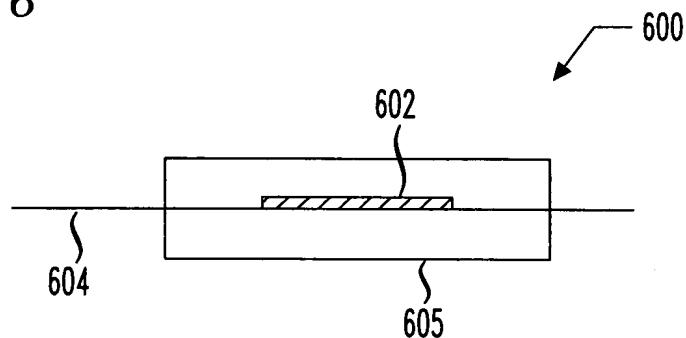


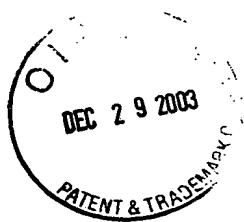
FIG. 6



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FIG. 7

